

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449A/PTO

(Use as many sheets as necessary)

Application Number	09/935,779
Filing Date	August 24, 2001
First Named Inventor	Frampton E. ELLIS
Art Unit	2152
Examiner Name	REILLY
Attorney Docket Number	313449-P0008

Sheet	1	of	3	Attorney Docket Number	313449-P0008
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Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ^{2 (if known)}			
AS	1	US -6,732,141	05/2004	ELLIS	
	2	US -6,287,949	09/2001	MORI et al.	
	3	US -6,440,775	08/2002	KHOURY	
	4	US -5,838,542	11/1998	NELSON et al.	
	5	US -5,861,817	01/1999	PALMER et al.	
	6	US -6,950,947	09/2005	PURTELL et al.	
	7	US -5,627,879	05/1997	RUSSELL et al.	
	8	US -6,208,634	03/2001	BOULOS et al.	
	9	US -5,896,499	04/1999	McKELVEY	
	10	US -6,073,209	06/2000	BERGSTEN	
	11	US -5,784,628	07/1998	RENTERIS	
	12	US -5,764,889	06/1998	AULT et al.	
AS	13	US -5,606,615	02/1997	LAPOINTE et al.	
		US -			
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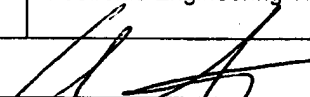
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Application Number	09/935,779	
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			Examiner Name	REILLY	
Sheet	2	of	3	Attorney Docket Number	313449-P0008

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
AS	14	Foster et al., "The Grid: Blueprint for a New Computing Infrastructure", Morgan Kaufman Publishers, Inc., 1998.		
	15	Hwang et al., "Scalable Parallel Computing", WCB McGraw-Hill, 1998.		
	16	Patterson et al., "Computer Architecture: A Quantitative Approach" (2nd Edition), Morgan Kaufmann Publishers, Inc., 1996.		
	17	Culler et al., "Parallel Computer Architecture", Morgan Kaufmann Publishers, Inc., 1998.		
	18	Hennessey et al., "Computer Organization and Design", Morgan Kauffmann Publishers, Inc., 1998.		
	19	Dallas Morning News; "LSI holds big plans for tiny chips Versatility of 'system on a chip' creates niche in microelectronics," March 4, 1996		
	20	Mokhoff; "System-on-a-chip comes to wireless arena;" Electronic Engineering Times; Feb 12, 1996		
	21	Cindi; "System on a Chip' stars at ISSCC;" Electronic News; Feb 19, 1996		
	22	Ang; "System-on-a-chip to define next-generation set-top box"; Electronic Engineering Times; Dec 15, 1995		
	AS			
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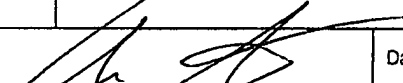
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AS	23	Marc; "New family of microprocessor cores from LSI Logic extends customers' system-on-a-chip design capability" Nov 7, 1994	
	24	Wall Street Journal; "Technology Brief - Advance Micro Devices Inc.: Company unveils Microchip for Hand-Held Computers"; Oct 18, 1993	
	25	Gelsinger, Patrick et al. "Microprocessors circa 2000," IEEE Spectrum, October 1989 pp. 43-47.	
	26	Yu, Albert. "The Future of Microprocessors," IEEE Micro, December 1996, pp. 46-53.	
	27	McWilliams. "Dell to Phase Out Computers Using Intel's Itanium," The Wall Street Journal, Online, September 15, 2005.	
	28	David PESCOVITZ, "Power of the PC", Scientific American, pp. 27-28 (April 2000)	
	29	Stephen H. WILDSTROM, "The Problem with Firewalls", Business Week, pp. 25 (March 20, 2000)	
	30	J. McH., "Build Your Own Supercomputer", FORBES, pp. 228 (November 15, 1999)	
	31	Office Action issued in related copending U.S. Patent Application No. 09/884,041, mailed January 30, 2007.	
	32	Office Action issued in related copending U.S. Patent Application No. 10/684,657, mailed March 1, 2007.	
	AS	33	Office Action issued in related copending U.S. Patent Application No. 10/802,049, mailed September 17, 2007.
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U. S. PATENT DOCUMENTS

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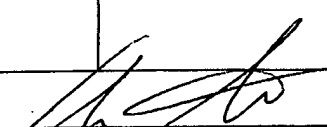
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AR		Office Action issued in related U.S. Patent Application No. 09/085,755, mailed December 12, 2007	
AB		Office Action issued in related U.S. Patent Application No. 09/884,041, mailed December 12, 2007	
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